## IN THE SPECIFICATION:

On Page 4, line 23, delete the words "try to retains" and replace it with the word "retain".

On Page 4, line 24, delete the words "the most" and replace them with the words "as many of the".

On Page 5, line 8, delete the word "are" and replace it with the word "is".

On Page 6, line 10, add the following:

## "BRIEF DESCRIPTION OF THE DRAWINGS:

Figure 1 shows in block diagram form an overview of the digital information processing system implementing the invention."

On Page 17, line 11, delete the word "is".

On Page 17, line 20, add the following after the word "implementation.": "As can be seen in Figure 1, digital information processing system 10 includes arithmetic unit 12 and memory 14. Arithmetic unit 12 typically contains VLSI binary logic circuit elements (such as adders, shifters, exclusive-or circuits, etc) which, under system control, receives binary divisor and dividend data from memory 14, performs the steps of the invention, and thereupon provides the resulting quotient data back to memory 14. Those skilled in the art of digital information processing systems can readily provide the interconnection of needed logic circuit elements (and their appropriate control) to implement the invention."

## IN THE DRAWINGS:

Add new Fig. 1 as shown in the enclosed drawing sheet.

21.

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